

Search Notes



Application/Control No.

10/689,592

Examiner

Lee, Chun-Kuan (Mike)

Applicant(s)/Patent under Reexamination

TANAKA, GENICHI

Art Unit

2182

SEARCHED

Class	Subclass	Date	Examiner
710	58	07/19/05	Chun-Kuan Lee
327	219	07/19/05	↓
327	252	07/19/05	
327	250	07/19/05	
327	100	07/19/05	
257	202	07/19/05	

INTERFERENCE SEARCHED

Class	Subclass	Date	Examiner

SEARCH NOTES (INCLUDING SEARCH STRATEGY)

	DATE	EXMR
Circuit#2 same (time#4 or delay) near 6 Information#2 OR Parameter#2 OR Value#2)) Same (cell near library)	07/19/05	Chun-Kuan Lee
Semiconductor same (measure#4 or generate#4) near 2 Timing near 2 (Information#2 OR Value#2))	07/19/05	↓
(HDL or Code) same Circuit#2 same (delay near 2 Time) same (input near 2 output near 2 (interconnect#4 OR Connection#2))	07/19/05	